

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|--|------------------|---------|------------------|
| S1 | 2 | "5587920".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 14:00 |
| S2 | 2 | "5838542".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 14:08 |
| S3 | 2 | "5861817".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 17:46 |
| S4 | 2 | "6950947".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 17:46 |
| S5 | 2 | "5627879".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 17:47 |
| S6 | 2 | "6208634".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 17:47 |
| S7 | 2 | "5896499".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 17:47 |
| S8 | 242 | parallel processing AND well known AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:27 |

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|-----|-------|--|---|-----|----|------------------|
| S9 | 21 | firewall AND parallel processing AND well known AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:33 |
| S10 | 4 | (data transmission NEAR speed) AND firewall AND (parallel processing OR multitasking OR multithreading) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:34 |
| S11 | 10 | (data transmission NEAR speed) AND (parallel processing OR multitasking OR multithreading) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:36 |
| S12 | 940 | (parallel processing OR multitasking OR multithreading) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:36 |
| S13 | 90 | firewall AND (parallel processing OR multitasking OR multithreading) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/11 18:37 |
| S14 | 15 | idle AND firewall AND (parallel processing OR multitasking OR multithreading) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:47 |
| S15 | 8 | user idle AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:50 |
| S16 | 32332 | parallel processing OR multitasking AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:51 |

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|-----|------|---|--|-----|----|------------------|
| S17 | 485 | distributed AND (parallel processing OR multitasking) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:51 |
| S18 | 96 | distributed computing AND (parallel processing OR multitasking) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:51 |
| S19 | 95 | network AND distributed computing AND (parallel processing OR multitasking) AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:59 |
| S20 | 1080 | erase AND memory AND power AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 07:59 |
| S21 | 837 | network AND (file OR data OR information) AND (erase OR erasure) AND memory AND power AND ("726".clas. OR "713".clas. OR "380".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:02 |
| S22 | 667 | network AND (file OR data OR information) AND (erase OR erasure) AND memory AND power AND ("726".clas. OR "713".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:02 |
| S23 | 457 | protection AND network AND (file OR data OR information) AND (erase OR erasure) AND memory AND power AND ("726".clas. OR "713".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:02 |
| S24 | 45 | protection AND network AND (file OR data OR information) AND ((erase OR erasure) NEAR memory) AND power AND ("726".clas. OR "713".clas.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:03 |

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| S25 | 2 | "6073209".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:30 |
| S26 | 2 | "5784628".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:30 |
| S27 | 2 | "5764889".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:30 |
| S28 | 2 | "5606615".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2007/09/12 08:31 |
| S29 | 18 | firewall AND faraday AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:36 |
| S30 | 3 | firewall AND microchip AND faraday cage AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:36 |
| S31 | 15 | firewall AND (micro\$1chip micro\$1processor processor CPU processing unit) AND faraday AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:37 |
| S32 | 9934 | firewall AND (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:39 |

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| S33 | 14 | inner firewall AND (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:39 |
| S34 | 14 | inner firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:39 |
| S35 | 2637 | access control AND firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:43 |
| S36 | 2622 | network AND access control AND firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:47 |
| S37 | 2 | (faraday shield faraday case) AND network AND access control AND firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:49 |
| S38 | 541 | frampton.in. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:49 |
| S39 | 16 | frampton.in. AND microchip AND microprocessor AND firewall | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:50 |
| S40 | 1 | microchip.clm. AND microprocessor.clm. AND inner.clm. AND firewall.clm. AND faraday.clm. AND cage.clm. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:51 |

EAST Search History

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| S41 | 1 | microchip.clm. AND microprocessor.clm. AND firewall.clm. AND faraday.clm. AND cage.clm. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:51 |
| S42 | 1 | microchip.clm. AND microprocessor.clm. AND firewall.clm. AND faraday.clm. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:52 |
| S43 | 13 | microchip.clm. AND microprocessor.clm. AND firewall.clm. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:52 |
| S44 | 43 | microchip AND faraday (cage shield) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/26 17:56 |
| S45 | 0 | processing unit\$1 AND control unit\$1 AND microchip AND faraday (cage shield) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 09:43 |
| S46 | 335 | processing unit\$1 AND control unit\$1 AND microchip AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 09:43 |
| S47 | 13 | firewall\$1 AND processing unit\$1 AND control unit\$1 AND microchip AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 09:43 |
| S48 | 0 | faraday (shield cage screen) AND firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) AND (@pd< "20010921" or @ad< "20010921" or @prad< "20010921" or @rlad< "20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:25 |

EAST Search History

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| S49 | 0 | faraday AND firewall AND (micro\$1chip micro\$1processor processor CPU processing unit\$1) AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:26 |
| S50 | 0 | faraday AND firewall AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:26 |
| S51 | 11 | ellis.in. AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:27 |
| S52 | 1 | faraday AND firewall AND ("380".clas. "726".clas. "713".clas.) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:28 |
| S53 | 4 | microchip.clm. AND microprocessor.clm. AND firewall.clm. AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:36 |
| S54 | 0 | microchip.clm. AND microprocessor.clm. AND inner.clm. AND firewall.clm. AND faraday.clm. AND cage.clm. AND (380/59.ccls. 726/2,11,16,26,27,34.ccls. 713/189,192.ccls.) | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/27 15:36 |
| S55 | 6 | "6725250".pn. "7024449".pn. "5905429".pn. | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 10:57 |
| S56 | 3946 | (photovoltaic cell solar cell light powered) AND (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:25 |

EAST Search History

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| S57 | 1576 | well known AND (photovoltaic cell solar cell light powered) AND (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:29 |
| S58 | 148 | (well known known in the art) SAME (photovoltaic cell solar cell light powered) AND (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:30 |
| S59 | 38 | (well known known in the art) SAME (photovoltaic cell solar cell light powered) SAME (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:31 |
| S60 | 2 | (well known known in the art) SAME (faraday cage faraday shield faraday screen) SAME (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:40 |
| S61 | 6 | (well known known in the art) SAME (faraday cage faraday shield faraday screen EMI shield EMI cage) SAME (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:41 |
| S62 | 44 | (well known known in the art) SAME (faraday cage faraday shield faraday screen EMI shield EMI cage electromagnetic interference) SAME (micro\$1chip micro\$1processor processor CPU processing unit) AND (@pd<"20010921" or @ad<"20010921" or @prad<"20010921" or @rlad<"20010921") | US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB | ADJ | ON | 2008/06/30 11:43 |